

ABSTRACT OF THE DISCLOSURE

A processor executes an initialization sequence that exchanges predetermined signals prior to data communication. A complex operation circuit multiplies transmission data of a sub-carrier by gain compensation data and phase angle compensation data, which are prepared for the same sub-carrier. Therefore, modulation is done so that the multiplication results are simultaneously transmitted, by an IFFT unit, in a plurality of sub-carriers. In addition, the transmission data of a sub-carrier, during the data communication, is multiplied by gain compensation data and phase angle compensation data, which are prepared for the same sub-carrier.